

Modelling and performance analysis of multilevel inverter for single-phase grid connected photovoltaic modules

Badoud Abd Essalam^{#1} and Khemliche Mabrouk^{#2}

[#] Automatic laboratory of Setif, Electrical engineering department, university of Setif 1,

City of Mabouda, Setif Algeria, 19000

¹badoudabde@yahoo.fr

³mabroukkhemliche@yahoo.fr

Abstract— Power electronic converters always have been circuits of difficult modeling because differential equations that describe them have discontinuities. Although this situation has been improved since the appearance of the bond graph approach, able to jointly describe both continuous and discrete behaviors exhibited by some physical systems, nowadays it is possible to obtain very precise models which help us in the study and design of such circuits. This work gives an overview on single-phase grid converter based on seven level diode clamped multilevel inverter for photovoltaic system with maximum power tracking. In the first part, we develop a bond graph model of the inverter. A bond graph MPPT strategy is developed, the performance of the algorithm is studied. Then, we present the bond graph model of solar cell. Thus, we study a cascade constituted by tow photovoltaic cell panels - five-level NPC VSI - permanent magnet synchronous machine (PMSM) fed by photovoltaic PV energy systems at different illuminations. At the full solar intensity, the maximum power point of current/voltage I/V characteristic of the PV modules is designed to be at the rated conditions of the machines. The steady-state output characteristics, the torque-speed characteristics, of the three DC motors with the two inputs are presented and compared.

Keywords—Bond Graph; Multilevel Inverter; Photovoltaic; Modelling; Permanent Magnet Synchronous Machine.

I. INTRODUCTION

The interest and importance in renewable energy has been aroused due to the Kyoto agreement on the global reduction of greenhouse emissions. Photovoltaic is one of the important renewable energy sources [1], [2], [3] and the cost of the photovoltaic is on falling trend and is expected to fall further as demand and production increases [4], [5]. The benefits of power generation from these sources are widely accepted. They are essentially inexhaustible and environmentally friendly.

Among the different renewable-energy sources possible to obtain electricity, solar energy has been one of the most active research are as in the past decades, both for grid-connected and standalone applications [6], [7]. The installed PV power has been increasing in the past, and a more significant increase is expected in the near future, owing to the potential advances in the PV conversion technology and the reduction

in cost-per-watt that a large-scale production will bring about [8].

Photovoltaic power supplied to the utility grid is gaining more and more visibility while the world's power demand is increases. Growing demand, advancements in semiconductor technology and magnetic materials such as high frequency inductor cores, has a significant impact on PV inverter topologies and their efficiencies, on the improvement of the control circuits on the potential of costs reduction. The user naturally wants to operate the Photovoltaic (PV) array at its highest energy conversion output by continuously utilizing the maximum available solar power of the array. The electrical system PV modules are powered by solar arrays requires special design considerations due to varying nature of the solar power generated resulting from unpredictable and sudden changes in weather conditions which change the solar irradiation level as well as the cell operating temperature. This work gives an overview on single-phase grid converter based on seven level diode clamped multilevel inverter for photovoltaic system with maximum power tracking. Multilevel voltage source inverters offer several advantages compared to their conventional counterparts. By synthesizing the AC output terminal voltage from several levels of voltages, staircase waveforms can be produced, which approach the sinusoidal waveform with low harmonic distortion, thus reducing filter requirement. The need of several sources on the DC side of the converter makes multilevel technology attractive for photovoltaic applications.

II. BOND GRAPH APPROACH

Bond graph, a graphical modeling language, provides a model formalism that decomposes the system into subsystems that map to the physical connections [9]. Bond graph provides a systematic way to model dynamic systems with different energy domains, such as electrical, hydraulic, mechanical, etc., in unified frame work. In bond graph modeling, a physical system can be described by bond graph components which include source elements Se and Sf , dissipative element R , storage elements C and I , four junctions 0 , 1 , TF and GY . These generalized BG components are connected by bonds, which represent energy exchange between two physical components. For each bond, there are two energy variables,

effort and flow, to describe the states of the physical components. BG modeling utilizes these BG components and their bond connections to describe the behavior of a physical system [10].

III. SYSTEM CONFIGURATION

The system configuration for the topic is as shown figure 1. Here the PV array is a combination of series and parallel solar cells. This array develops the power from the solar energy directly and it will be changes by depending up on the temperature and solar irradiances. So we are controlling this to maintain maximum power at output side we are boosting the voltage by controlling the current of array with the use of PI controller. By depending upon the boost converter output voltage this AC voltage may be changes and finally it connects to the utility grid that is which feeds a PMSM. Here we are using seven-level diode clamped multilevel inverter to obtain AC output voltage from the DC boost output voltage.

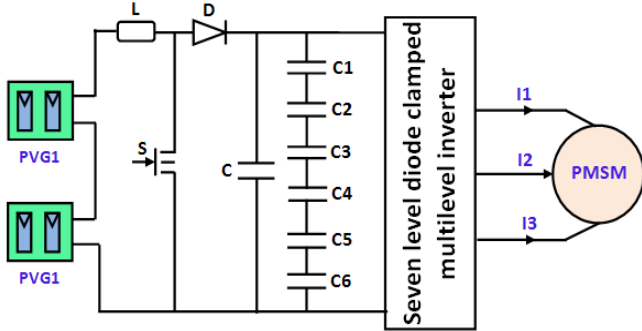


Fig. 1 Two photovoltaic generator single-phase grid converter seven level diode clamped multilevel inverter

IV. MODELLING OF PHOTOVOLTAIC SYSTEM

A. Bond graph modelling of PV array

Several models of solar cell exist to calculate their electric characteristics [11, 12, 13, 14, 15, 16]. To improve the accuracy, model parameters such as irradiance correction factor and Temperature coefficient of a series resistance are discussed in [17].

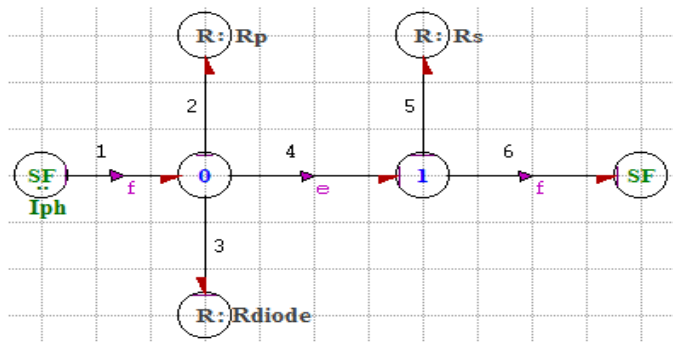


Fig. 2 Bond graph model of PV

All these models have the disadvantage that required parameters are not published in standard data sheets. Thus it is of interest to have a simpler model to calculate the electrical

properties of photovoltaic modules with only those data that are available from data sheets. For the bond graph representation, the PV generator is then modelled by a flow source $S_f = I_{ph}$ in parallel with the resistors R_p , the whole followed by a serial resistance R_s . The PV diode bond graph representation is a non-linear resistor R_{diode} .

B. Boost Converter

The boost converter which has boosting the voltage to maintain the maximum output voltage constant for all the conditions of temperature and solar irradiance variations. A simple boost converter is as shown in figure (3). The boost converter contains an IGBT and a diode which are represented as a dual ideal switch U in order to simplify the circuit analysis. If U is a state of 0, the diode is on and the IGBT is off and vice versa if U is a state of 1.

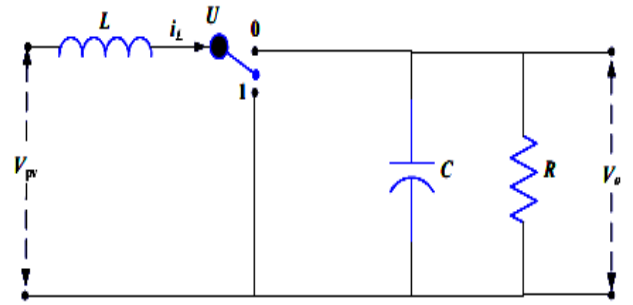


Fig. 3 Boost converter circuit diagram.

The boost converter contains also passive components: an inductor L , an capacitor C and a resistance R . The operation principle of the boost converter can be demonstrated for each switching period under the continuous conduction mode (CCM) into two modes [18]: the first mode is an ON mode in the duration the period $0 \leq t \leq t_{on}$ and its state equations can be represented as follows:

$$\begin{cases} L \frac{di_L}{dt} = V_{pv} \\ C \frac{dV_0}{dt} + \frac{V_0}{R} = 0 \end{cases} \quad (1)$$

Where t_{on} is the ON mode time and i_L the continues inductor current. The second mode is an OFF mode in the duration $t_{on} \leq t \leq T_s$ and its state equations can be represented as the following:

$$\begin{cases} L \frac{di_L}{dt} + V_0 = V_{pv} \\ i_L - C \frac{dV_0}{dt} - \frac{V_0}{R} = 0 \end{cases} \quad (2)$$

Where T_s is the switching period. The design of a boost converter for a PV system is a complex task which involves many factors. In general, the input and output voltages of the boost converter are varied with the solar irradiances and load variations. The output voltage is also varied MPPT controller. Thus the selection of boost converter components (the input

inductor and the output capacitor) is a compromise between dynamic responses and the MPPT algorithm trigger time. The maximum value of the state variables should be calculated to estimate the value of the boost converter [18].

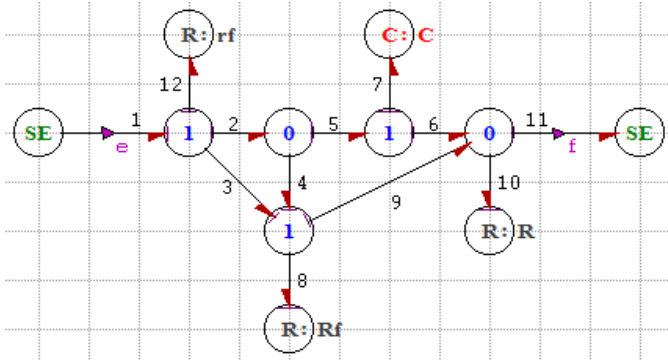


Fig. 4 Bond graph model of boost converter.

C. Maximum power point tracking

The proposed integrated Maximum Power Point Tracker (MPPT) has been used to force the PV array to work around the maximum power point. For this reason, the MPPT is required to track the maximum power available in the PV array. The MPPT operates by periodically incrementing the terminal voltage of the PV array and continuously seek. The radiation and temperature are used to calculate the maximum PV array output power and PV array terminal voltages. The MPPT operates by periodically incrementing the terminal voltage of the PV array and continuously seek the peak power point. The control system adjusts the boost converter to seek maximum power point of PV array. Changing the duty ratio according to the error signal between the maximum and actual power will pass the maximum power available from PV system to the electric utility. The bond graph control scheme (BG-MPPT) for the three-phase grid-connected photovoltaic energy conversion system is depicted in figure (5).

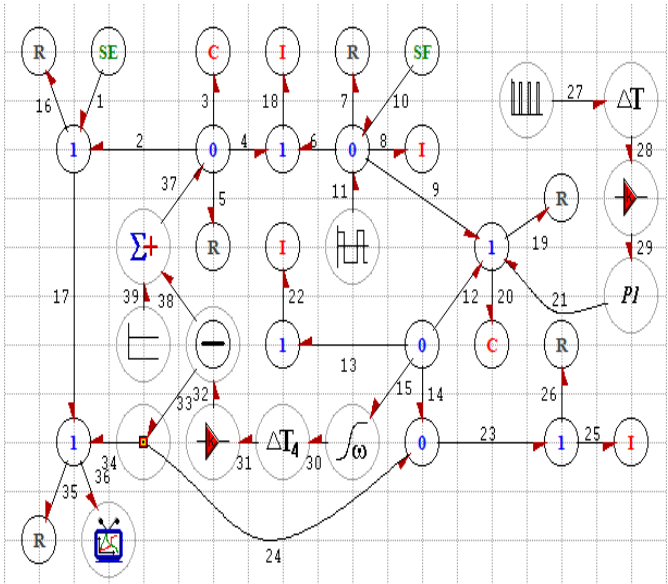


Fig. 5 The bond graph MPPT control of the global system

D. Multilevel Inverter topology

Multilevel converters have many advantages for medium and high power systems as they synthesize a higher output voltage than the voltage rating of each switching device. Stepped output voltage allows a reduction in harmonic content of voltage and current waveforms, switching frequency, and semiconductors voltage level. The presented merits make multilevel converters appropriate for medium and high voltage renewable energy applications. The best known multilevel topologies [19] are diode-clamped, flying capacitor, and cascade inverter, the latter being normally considered for PV application. However, the diode-clamped converter is the most popular converter in renewable energy system due to its structure. Different current [20], [21] and voltage control [22] techniques have been proposed for multilevel converters to have an optimum efficiency. Although each type of multilevel converters share the advantages of multilevel voltage source inverters, they may be suitable for specific application due to their structures and drawbacks. This work is interested on seven level diode clamped multilevel inverter. Concept of the diode clamped topology was proposed by Nabae [23]. This topology has found wide acceptance for its capability of high voltage, and high efficiency operation. A phase leg of a three level diode-clamped inverter is shown in figure (7) it consists of two pairs of switches and two diodes.

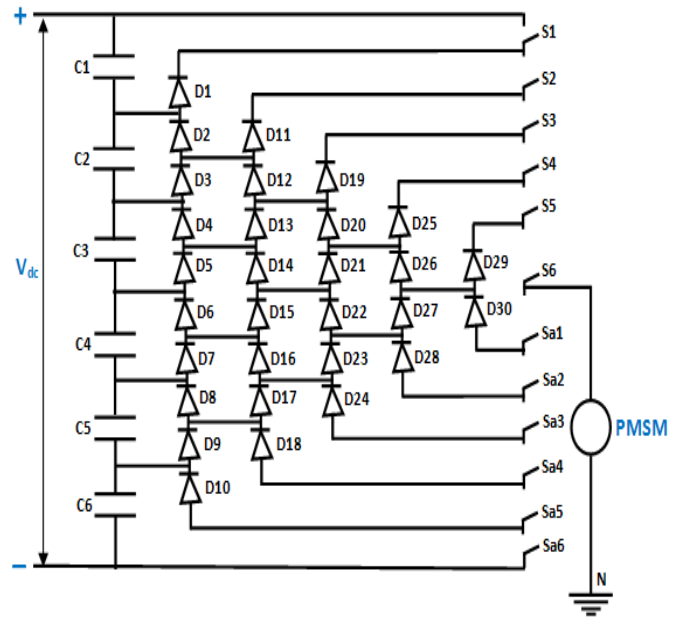


Fig. 6 Simple single-phase bridge inverter circuit

In this paper seven-level diode clamped multilevel inverter. A diode clamped multilevel inverter for m-level consists of m-1 capacitors, 2(m-1) switching devices on the DC bus and produces m levels operation on the phase voltage. So the seven-level diode clamped multilevel inverter consisting of twelve switching devices (S1-S6, Sa1-Sa6), ten clamping diodes (D1-D10), six capacitors (C1-C6), provides seven voltage levels. Assuming the DC source voltage V_{dc} is evenly split by the capacitors can be produced by turning on simultaneously the switch combinations of (S1 S2 S3 S4 S5

S6), (S2 S3 S4 S5 S6 Sa1), (S3 S4 S5 S6 Sa1 Sa2), (S4 S5 S6 Sa1 Sa2 Sa3), (S5 S6 Sa1 Sa2 Sa3 Sa4), (S6 Sa1 Sa2 Sa3 Sa4 Sa5) and (Sa1 Sa2 Sa3 Sa4 Sa5 Sa6) respectively. Seven switch combinations where always six switches are switched simultaneously generate seven different voltage levels at the AC output of the inverter. By adding more levels on the DC bus the numbers of levels of the voltage at the inverter output terminals are also increased. This allows for reduced

distortion of the output waveform. Tables must be numbered using uppercase Roman numerals. Table captions must be centred and in 8 pt Regular font with Small Caps. Every word in a table caption must be capitalized except for short minor words as listed in Section III-B. Captions with table numbers must be placed before their associated tables, as shown in Table 1.

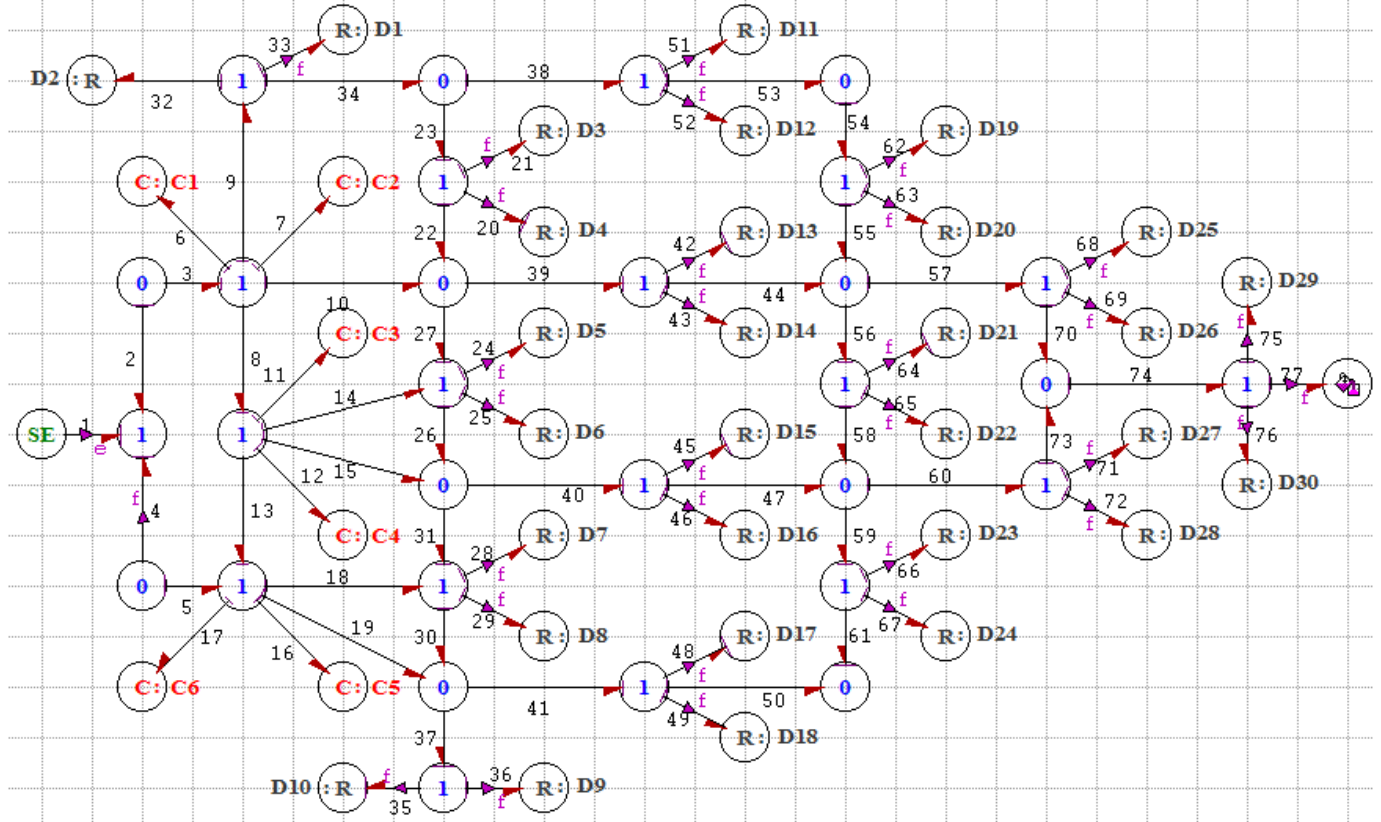


Fig. 7 Bond graph of simple single-phase bridge inverter circuit

E. Permanent magnet synchronous motor

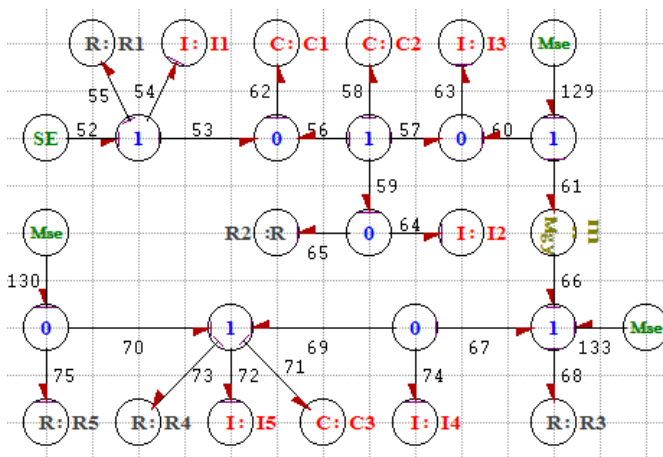


Fig. 8 Bond graph model of PMSM

The diagram below represents bond graph model of a permanent magnet synchronous motor (PMSM) by presenting some parameters held in account. Rotor speed and the moment of inertia is modelled by I, resistance stator, rotor, frictions are modelled by R, the condensers, the mechanical structures are modelled by C, electromechanical transformation modelled by a gyrator GY and the electric source by SE. The figure (8) gives the bond graph model of a PMSM.

V. SIMULATION RESULTS

The converter circuit topology is designed to be compatible with a given load to achieve maximum power transfer from the solar arrays. The power will be maximum for along the variations of temperature and solar irradiance in the PV array which is giving to input to seven-level diode clamped multilevel inverter.

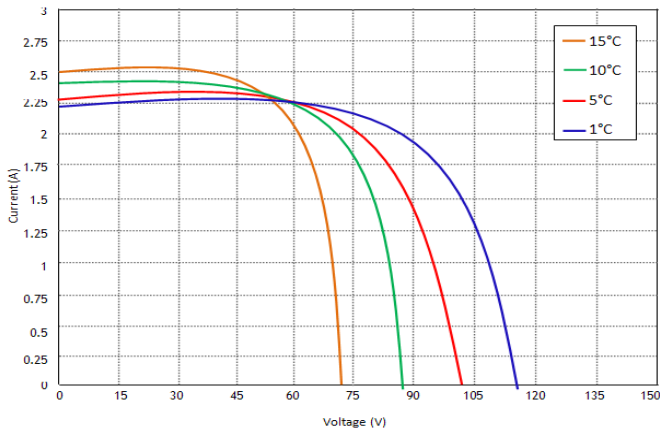


Fig. 9 I-V characteristics of PV module for various temperatures at rated solar intensity

We observed that the designed seven-level diode clamped multilevel inverter successfully followed the variations of solar irradiation. The maximum power point tracking controls their respective voltage, current waves. Here the power is maintaining maximum value and similarly the boost converter boosting the voltage under the control of the MPPT. By this, PV array, boost converter output voltages are converted to AC voltages which are supplied to the grid by using seven-level diode clamped multilevel inverter and its characteristics also mentioned here.

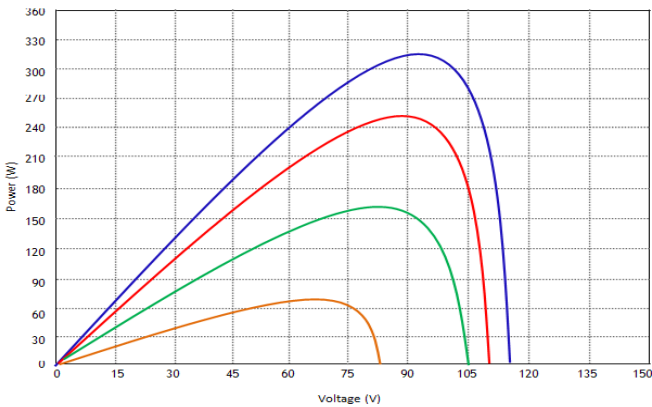


Fig. 10 P-V characteristic of PV module

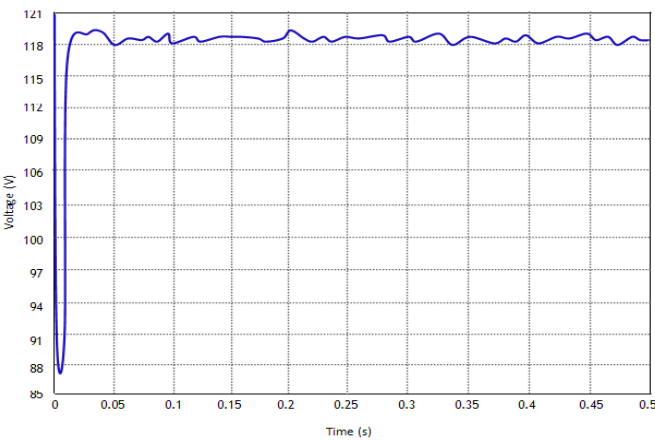


Fig. 11 Input voltage of boost converter

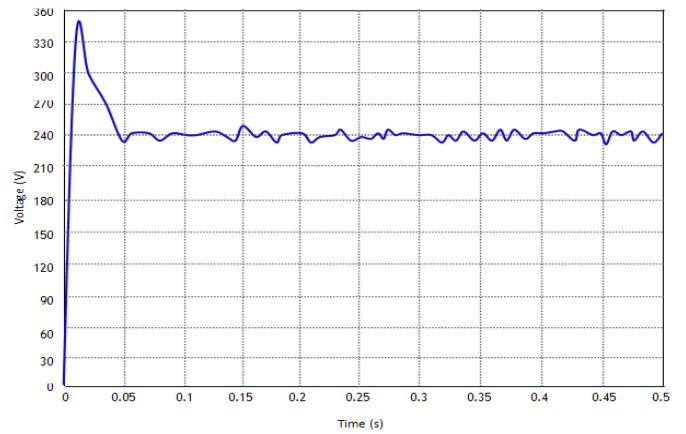
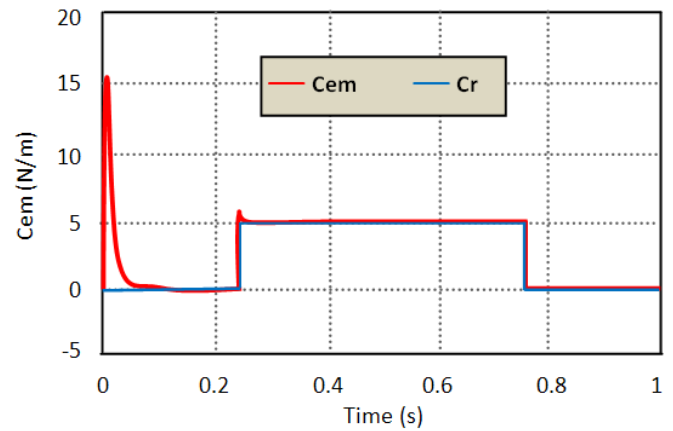
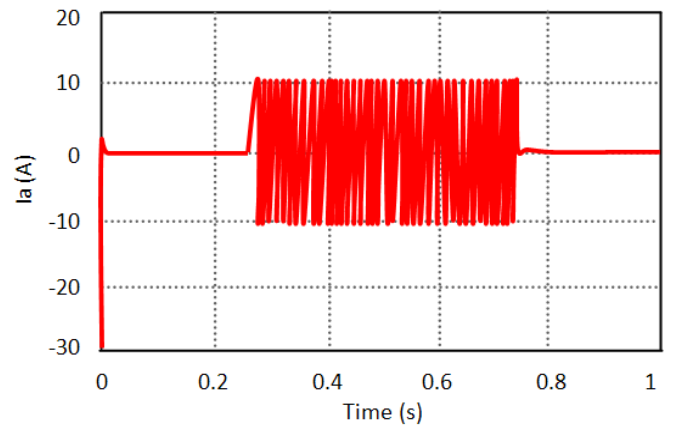
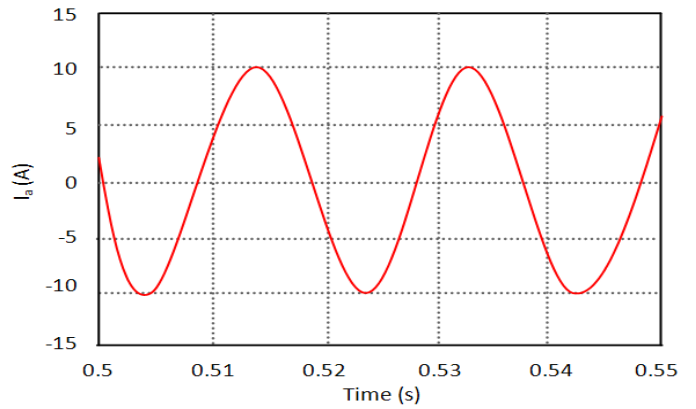


Fig. 12 Output voltage of boost converter



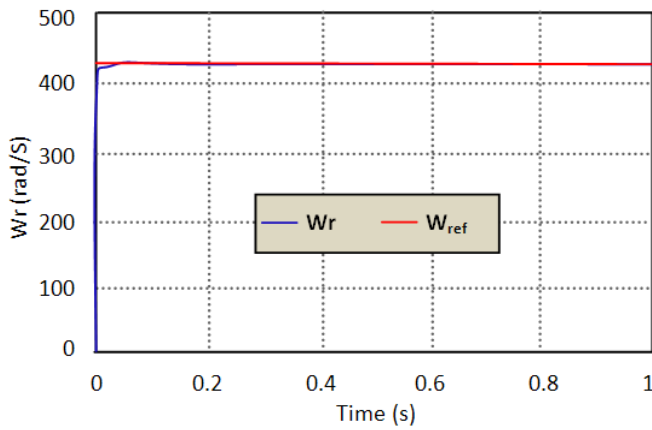


Fig. 13 Performance of PMSM

The performance of the speed control algorithm of the PMSM shows that the current of the machine nearly is sinusoidal. The speed and the torque effect for the charge variation between two instants $t=1.5s$ and $t=2.5s$ (Figure 13).

VI. CONCLUSIONS

The present work proposes a general bond graph structure for linear and nonlinear modeling of photovoltaic layers with polarization through their thickness, which can handle the most important physical phenomena in their behavior. In order to consider the power transformation between the irradiation and temperature and its effect on the photovoltaic material, the model has an intermediate domain where the dipole and electric field in the material are defined. This paper, we have studied the performances of multilevel inverter for single-phase grid connected photovoltaic modules. Multilevel voltage source inverters offer several advantages compared to their conventional counterparts. By synthesizing the AC output terminal voltage from several levels of DC voltages, staircase wave forms can be produced, which approach the sinusoidal waveform with low harmonic distortion, thus reducing filter requirements. The need of several sources on the DC side of the converter makes multilevel technology attractive for photovoltaic applications.

REFERENCES

[1] B. Kroposki and R. DeBlasio, "Technologies for the new millennium: photovoltaics as a distributed resource," in *Proc. IEEE Power Engineering Society Summer Meeting*, Vol. 3, July 2000, pp. 1798-1801.

[2] M. P. Choi and A. Tan, "Photovoltaics Demonstration Projects," *Proc. of EMPD 98*, Vol. 2, 1998, pp.637-643.

[3] L. Castaner and S. Silvestre, "Modeling Photovoltaic System," John Wiley & Sons Ltd, 2002.

[4] J. M. Carrasco, L. G. Franquelo, J. T. Bialasiewicz, E. Galvan, R. C. Portillo Guisado, M. A. M. Prats, J. I. Leon, and N. Moreno-Alfonso, "Power-electronic systems for the grid integration of renewable energy sources: A survey," *IEEE Trans. Ind. Electron.*, vol.53, no.4, pp.1002-1016, Jun. 2006.

[5] F. Blaabjerg, Z. Chen, and S. B. Kjaer, "Power electronics as efficient interface in dispersed power generation systems," *IEEE Trans. Power Electron.*, vol. 19, no. 5, pp. 1184-1194, Sep. 2004.

[6] E. Roman, R. Alonso, P. Ibanez, S. Elorduizapatarietxe, and D. Goitia, "Intelligent PV module for grid-connected PV systems," *IEEE Trans. Ind. Electron.*, vol. 53, no. 4, pp. 1066-1073, Jun. 2006.

[7] R. J. Wai, W. H. Wang, and C. Y. Lin, "High-performance standalone photovoltaic generation system," *IEEE Trans. Ind. Electron.*, vol. 55, no.1, pp. 240-250, Jan. 2008.

[8] S. B. Kjaer, J. K. Pedersen, and F. Blaabjerg, "A review of single-phase grid-connected inverters for photovoltaic modules," *IEEE Trans. Ind. Appl.*, vol. 41, no. 5, pp. 1292-1306, Sep./Oct. 2005.

[9] Karnopp, D., & Rosenberg, R. (1968). *Analysis and simulation of multiport systems: The bond graph approach to physical system dynamics*. Cambridge, MA: MIT Press.

[10] Karnopp, D. C., Margolis, D. L., & Rosenberg, R. C. (2006). *System dynamics modeling and simulation of mechatronics systems*. New Jersey: John Wiley & Sons.

[11] International Electrotechnical Commission: IEC 60891 - Procedures for temperature and irradiance corrections to measured I-V characteristics of crystalline silicon photovoltaic devices, Ed. 1.0, 1987

[12] Anderson, A.J.: Final report for task 2.0, NREL subcontract no. TAD-4-14166-01, Oak Leaf Place, 1995.

[13] Blaesner, G.: PV array data translation procedure, *Proceedings of the 13th EC PVSC*, 1995

[14] Coors, S., Boehm, M.: Application of the two-exponential model to correction procedures for silicon solar cells, *Proceedings of the 1st EuroSun*, 1996, pp. 614-619

[15] Muellejans, H. et al: Reliability of routine 2-diode model fitting of PV modules, *Proceedings of the 19th European Photovoltaic Conference 2004*

[16] Wikipedia: Solarzelle - Erweitertes Ersatzschaltbild, in German, <http://de.wikipedia.org/wiki/Solarzelle>

[17] Delahaye, A.: I-V curve translation procedures, *SP4 Performance meeting*, October 5th 2006 at CREST, Loughborough, GB

[18] R. W. Erickson, *Fundamentals of Power Electronics*, 2nd ed., USA: Kluwer Academic Publishers, 2000.

[19] Rodriguez J., Jai J. Sh., and Peng F. Zh.; Multilevel inverters: a survey of topologies controls, and applications. *IEEE transaction on industrial electronics*, vol. 49, N° 4, 2002, pp. 724-738.

[20] Zare F., Ledwich G.; A hysteresis current control for single phase multilevel voltage source inverters: PLD implantation. *IEEE transaction on power electronics*, vol. 17, N° 5, 2002, pp. 731-738.

[21] Zare F., Ledwich G.; A new predictive current control technique for multilevel converters. *Australian journal of electrical and electronics engineering (AJEEE)*, 2008, vol. 4, N° 1, pp. 25-35.

[22] Leon J., Portillo R., Vazquez S., Padilla J. J., Franquelo L. J., and Carrasco J. M.; Simple unified approach to develop a time-domain modulation strategy for single phase multilevel converters. *IEEE transactions on industrial electronics*, vol. 55, N° 9, September 2008, pp. 3239-3248.

[23] Nabae A., Takahashi I., and Akagi H. A new neutral-point-clamped PWM inverter. *IEEE transaction on industrial Electronic*, Vol. 1A-17, N° 5, pp. 518-523, 1981.